

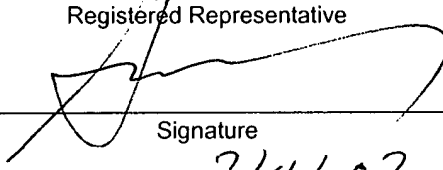


CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope, with sufficient postage, addressed to: Commissioner for Patents, Washington, D.C. 20231, on

3/4/03
Date of Deposit

Gustavo Siller, Jr.
Name of Applicant, Assignee or
Registered Representative


Signature

3/4/03
Date of Signature

Our Case No.: 9281-4101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Akiyuki Yoshisato et al.

Serial No.: 09/870,129

Filing Date: May 29, 2001

For: ELECTRONIC CIRCUIT SUITABLE
FOR MINIATURIZATION

Examiner: David E. Graybill

Group Art Unit No.: 2827

FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this First Supplemental Information Disclosure Statement be entered and the document listed below and on the attached Form PTO-1449 be considered by the

Examiner and made of record. A copy of the listed document as required by 37 C.F.R. § 1.98(a)(2) is enclosed.

OTHER ART

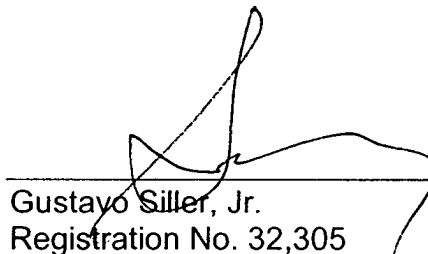
H. Samavati, A. Hajimiri, A. R. Shahani, G. N. Nasserbahkt and T. H. Lee, "Fractal Capacitors", IEEE Journal of Solid-State Circuits, vol. 33, no. 12, IEEE Inc., New York, US, December 1998, pp 2035-2041.

In accordance with 37 C.F.R. § 1.97(g), (h), this First Supplemental Information Disclosure Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In conformance with 37 C.F.R. § 1.97(e)(2), Applicants state that the document cited in this disclosure statement was not cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the signatory to this submission, no information contained in the information disclosure statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this statement. No fees are believed to be due in connection with filing of this First Supplemental Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these material, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925. A duplicate copy of this document is enclosed.

Applicants respectfully request that the listed document be made of record in the present case.

Respectfully submitted,



Gustavo Siller, Jr.
Registration No. 32,305
Attorney for Applicant(s)

BRINKS HOFER GILSON & LIONE
P.O. Box 10395
Chicago, IL 60610
(312) 321-4200



FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	SERIAL NO. To be Assigned	CASE NO. 9284-4101
	FILING DATE May 29, 2001	GROUP ART UNIT 2827
	APPLICANTS: Akiyuki Yoshisato et al.	

REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS				
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
	A1	H. Samavati, A. Hajimiri, A.R. Shahani, G.N. Nasserbahkt and T.H. Lee, "Fractal Capacitors", IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, IEEE Inc., New York, U.S., December 1998, pp. 2035-2041.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.